

Vi Ngoc-Nha Tran
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Professional Experience

5/2018 – 5/2021

Post-doctor Researcher at Department of Pharmacy, UiT The Arctic University of Norway

- JPI-AMR “Collateral Damage: Using collateral sensitivity to reverse the selection and transmission of antibiotic resistance”: Research on mathematical modeling of the mechanism of collateral sensitivity from different antibiotics.

1/2014 – 7/2014

Visiting PhD Fellow at Department of Computer Science, Rutgers University, USA

- Research on mathematical models of energy consumption and programming framework for computing systems.

5/2012 – 5/2013

Researcher of Performance Engineering Lab at University College Dublin, Ireland

- TRAFFIC project - Design and Develop Scalable Communication and Infrastructure Management Solutions for Intelligent Transportation Systems in Smart Cities (IBM Dublin project).

3/ 2011 – 3/2012

Software Engineer at CASSIS International, Singapore

- Design and Develop Global Platform (GP) Messaging Java API for Servlet, support provisioning via Over-the-air to Smart Phone Mobile Secure Element.

9/2009-12/2009

Software Engineer Intern at Fraunhofer IESE, Germany

- John Deere project – Farming Management System for Mobile Devices.

Education

- PhD: 10/2013 – 4/2018

PhD in Computer Science at UiT The Arctic University of Norway

- MSc: 2008 - 2010

European Master in Software Engineering (EMSE) at Technical University of Kaiserslautern (TUKL), Germany and Blekinge Institute of Technology (BTH), Sweden

- BSc: 2002 – 2007

BSc in Computer Science and Engineering at HCMC University of Technology, Vietnam

Awards and Grants

- Received HiPEAC grant to attend HiPEAC ACACES 2015 summer school
- Received Euro-TM COST Action grant to attend HiPEAC Euro-TM Workshop 2015
- Awarded Erasmus Mundus full scholarship for studying European Master in Software Engineering (EMSE) program (2008 – 2010)

Teaching Activities

- Guest lecture of INF-3910 Computer Science Seminar: Green Computing course, Spring 2016, UiT on the topic of "Energy models for parallel algorithms".
- Teaching Assistant of "Data Structure and Algorithm" course, Autumn 2012, University College Dublin.

Publications

1. Vi Ngoc-Nha Tran, Tommy Oines, Alexander Horsch, Phuong H. Ha. *REOH: Using Probabilistic Network for Runtime Energy Optimization of Heterogeneous Systems*. Proceedings of the 22nd IEEE International Conference on Parallel and Distributed Systems (ICPADS '18), 2018, page to appear, IEEE.
2. Vi Ngoc-Nha Tran and Phuong H. Ha. *ICE: A General and Validated Energy Complexity Model for Multithreaded Algorithms*. Proceedings of the 22nd IEEE International Conference on Parallel and Distributed Systems (ICPADS '16), 2016, pp. 1041-1048, IEEE
3. Vi Ngoc-Nha Tran, Brendan Barry and Phuong H. Ha. *Power Models Supporting Energy-efficient Co-design on Ultra-low Power Embedded Systems*. Proceedings of the International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation (SAMOS XVI), 2016, pp. 39-46, IEEE.
4. Vi Ngoc-Nha Tran, Brendan Barry and Phuong H. Ha. *RTHpower: Accurate Fine-grained Power Models for Predicting Race-to-halt Effect on Ultra-low Power Embedded Systems*. Proceedings of the 17th IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS '16), 2016, pp. 155-156, IEEE.
5. Vi Ngoc-Nha Tran, Patrick McDonagh, John Murphy. *Gathering Transportation Data by Acoustic Monitoring: A Case Study*. Proceedings of the 12th Information Technology & Telecommunications (IT&T) Conference, 2013, pp. 31-38.
6. Vi Ngoc-Nha Tran, Soufiene Djahel, John Murphy. *A comparative study of vehicles' routing algorithms for route planning in smart cities*. Proceedings of the first International Workshop on Vehicular Traffic Management for Smart Cities, 2012, pp.1-6, IEEE.
7. Frank Elberzhager, Jürgen Münch, Vi Ngoc-Nha Tran. *A systematic mapping study on the combination of static and dynamic quality assurance techniques*. Information & Software Technology Journal 54(1): 1-15 June 2011.

References

- Pia Abel zur Wiesch (post-doc advisor)
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